FIG. 2

17

A/D CONVERTER OUTPUT
(SAMPLE VALUE)

21 CROSS DETECTION PART

22 PHASE ERROR ESTIMATION PART

23 PATTERN DETECTION PART

24 SELECTION PART

0

/3T

20 PHASE ERROR DETECTION PART

PHASE ERROR SIGNAL

30 CONTROL PART

CONTROL SIGNAL

40 CLOCK GENERATION PART

CLOCK